



IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

SHF
1765

Applicant: Hiroyuki Enomoto

Docket TIJ-24816

Serial No.: 09/019,087

No.:

Examiner: Perez Ramos, Vanessa

Filed: 02/05/1998

Art Unit: 1765

For: MANUFACTURING METHOD OF SEMICONDUCTOR IC DEVICE

CONF. NO. 1640

H29
11/11/02
NW

NOTICE OF APPEAL

Assistant Commissioner For Patents

Washington, DC 20231

MAILING CERTIFICATE UNDER 37 C.F.R. §1.8(A)

I hereby certify that the above correspondence is being deposited with the US Postal Service as First Class Mail in an envelope addressed to: Assistant Commissioner for Patents, Washington, DC 20231

William B. Kempler, Reg. No. 28,228 Date

Sir:

Applicant hereby appeals to the Board of Appeals from the decision dated **10/10/2002**, of the Primary Examiner finally rejecting Claims **1-11**.

Charge the fee of \$320.00 to the deposit account of Texas Instruments Incorporated, Account No. 20-0668. **An original and two copies of this sheet are enclosed.**

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Respectfully submitted,

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OCT 30 2002

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